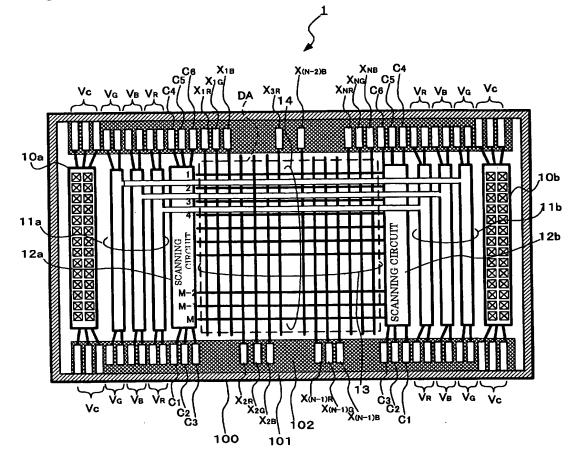
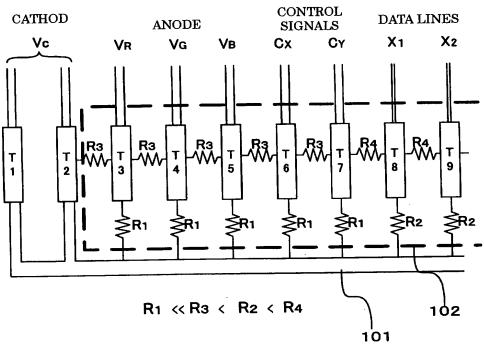


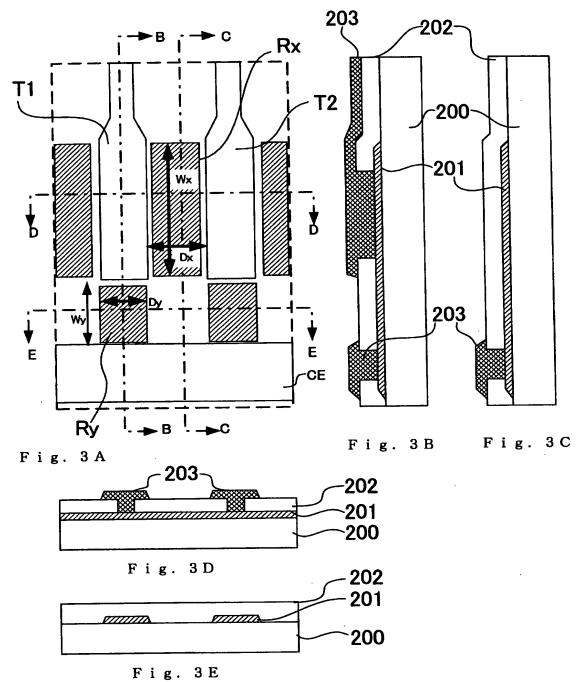
F i g. 1





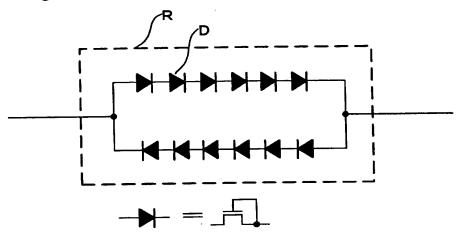








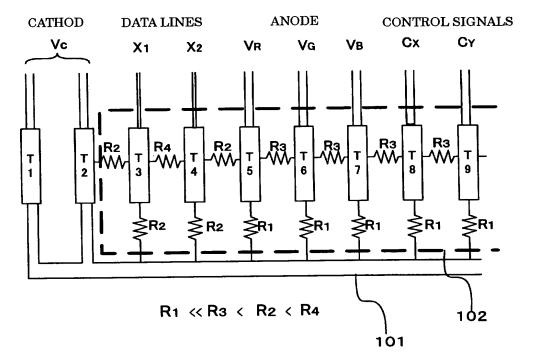
F i g. 4



F i g. 5 ${\tt CONTROL}$ ANODE DATA LINES CATHODE SIGNALS Vc Cx Vв X1 X2 VG CY VR R1 «R3 < R2 < R4 1Ò2 101



F i g. 6

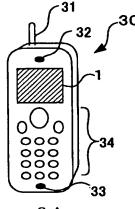




Vsig Vdd Vsi

TERMINAL GROUP





F i g. 8A

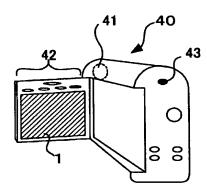
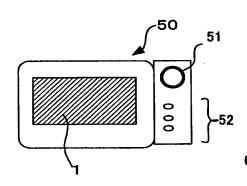


Fig. 8B



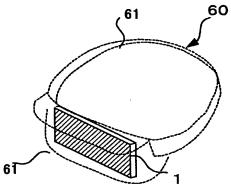


Fig. 8D

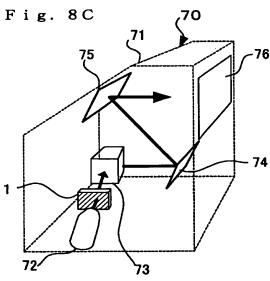
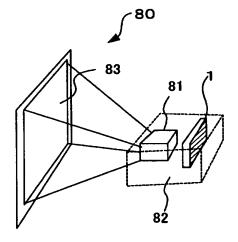


Fig. 8E



F i g. 8 F